

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

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| Title of Invention | A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR GENERATING AND VERIFYING ISOLATION LOGIC MODULES IN DESIGN OF INTEGRATED CIRCUITS |
| Application Number : | |
| Date : | |
| First Named Applicant: | Bhanu KAPOOR |
| Confirmation Number: | |
| Attorney Docket Number: | Q82851 |

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| Submitted By: | Elec. Sign. | Sign. Capacity |
|---|---------------|----------------|
| Kelly G. Hyndman Registered Number: 39,234 | /KGH20040921/ | Attorney |

| Documents being submitted: | Files |
|-----------------------------------|---|
| us-fee-sheet | Q82851-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd |
| us-ids | Q82851-usidst.xml us-ids.dtd us-ids.xsl |
| us-request | Q82851-usrequ.xml us-request.dtd us-request.xsl |
| us-assignment | Q82851-usassn.xml us-assignment.xsl us-assignment.dtd |
| us-declaration | Q82851asg.tif |
| us-declaration | Q82851dec1.tif |
| application-body | Q82851dec2.tif ABX1.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif Image5.tif Image6.tif Image7.tif Image8.tif Image9.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogrk1.ent isogrk2.ent isogrk3.ent isogrk4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd |

| | |
|---------------------------|-------------------------|
| | mathml2-qname-1.mod |
| | mmlalias.ent |
| | mmlextra.ent |
| | soextblk.dtd |
| | us-application-body.xsl |
| | wipo.ent |
| application-body-pdf-wrap | ABX1-pdf-wrap.xml |
| abstract-pdf | ABX1-abst.pdf |
| claims-pdf | ABX1-clms.pdf |
| description-pdf | ABX1-desc.pdf |
| drawings-pdf | ABX1-draw.pdf |
| Comments | |